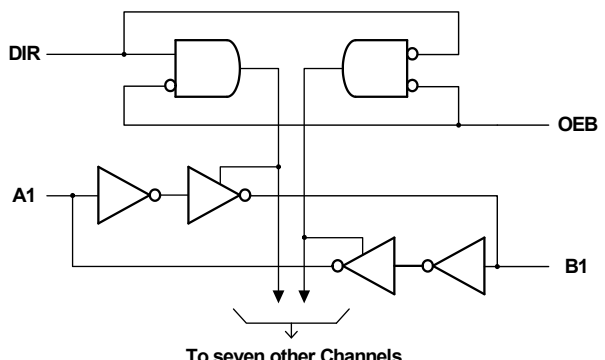


GT74LVC8T245

8-Bit Dual-Supply Bus Transceiver with Voltage Translation

1 Features	2 Application
<ul style="list-style-type: none"> - Control inputs VIH/VIL levels are referenced to VCCA - VCC isolation feature – if either VCC input is at GND, all are in the high-impedance state - Fully configurable dual-rail design allows each port to operate over the full 1.65V to 5.5V power supply range - Recommended temperature range: -40°C to +125°C - Max data rates <p>420 Mbps (3.3-V to 5-V Translation)</p> <p>210 Mbps (Translate to 3.3V)</p> <p>140 Mbps (Translate to 2.5V)</p> <p>75 Mbps (Translate to 1.8V)</p>	<ul style="list-style-type: none"> - Personal electronic - Industrial - Enterprise - Telecom

3 Description	Circuit Diagram
<p>This 8-bit noninverting bus transceiver uses two separate power-supply rails. The GT74LVC8T245 is optimized to operate with VCCA and VCCB set at 1.65V to 5.5V. The A port is designed to track VCCA. VCCA accepts any supply Voltage from 1.65V to 5.5V. The B port is designed to track VCCB. VCCB accepts any supply Voltage from 1.65V to 5.5V. This allows for universal low-voltage bidirectional translation between any of the 1.8V, 2.5V, 3.3V, and 5.5V Voltage nodes. The logic levels of the direction-control (DIR) input and the output-enable (OEB) input activate either the B-port outputs or the A-port outputs or place both output ports into the high-impedance mode. The device transmits data from the A bus to the B bus when the B-port outputs are activated, and from the B bus to the A bus when the A-port outputs are activated. The input circuitry on both A and B ports is always active and have a logic HIGH or LOW level applied to prevent excess I_{CC} and I_{CCZ}. The GT74LVC8T245 is designed so that the control pins are supplied by VCCA.</p>	 <p>The circuit diagram shows the internal logic of the transceiver. It features a DIR (Direction Control) input and an OEB (Output Enable) input. The DIR input is connected to two 2-input AND gates. The OEB input is connected to two 2-input AND gates. The A1 input is connected to a buffer and an AND gate. The B1 output is connected to a buffer and an AND gate. The circuit is designed to allow bidirectional data flow between the A and B buses, controlled by the DIR and OEB inputs. The diagram also shows connections to seven other channels.</p>

4 Revision History

Revision	Date	Note
Rev.A1.0	2023. 10. 21	Original Version
Rev.A1.1	2023. 10. 23	1.Update Package Qty 2.Added Tape and Reel Information 3. Added Application Note
Rev.A1.2	2023. 12. 26	1. Added Marking 2.Added MSL
Rev.A1.3	2024. 01. 26	Updated Part Name

The latest datasheet Version should be checked on the GTIC official website, as the company does not actively inform customers about updates to the datasheet.

5 Device Summary, Pin and Packages

Table.5-1. Device summary⁽¹⁾

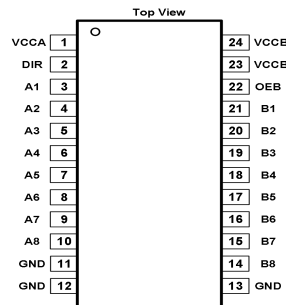
Serial Name	Part Name	Package	Body Size (Nom)	Marking ⁽²⁾	MSL ⁽³⁾	Package Qty
GT74LVC8T245	GT74LVC8T245TH	TSSOP-24	7.80mmx4.40mmx1.20mm	GT74LVC8T245 XXXXXXX	3	Tape and Reel,4000
GT74LVC8T245	GT74LVC8T245PH	SOP-24	15.40mmx7.50mmx2.54mm	GT74LVC8T245 XXXXXXX	3	Tape and Reel,1200

(1)For all available packages, please contact product sales.

(2)There may be additional marking, which relates to the lot trace code information (data code and Vendor code), the logo or the environmental category on the device.

(3)MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

(4)"XXXXX" in Marking will be appeared as the batch code.



**Fig.5-1. GT74LVC8T245: TH (TSSOP-24) Package
GT74LVC8T245: PH (SOP-24) Package**

Table.5-2. Pin Definition

PIN		I/O	Description
Name	TH/PH		
A1	3	I/O	Input/output A1. Referenced to VCCA.
A2	4	I/O	Input/output A2. Referenced to VCCA.
A3	5	I/O	Input/output A3. Referenced to VCCA.
A4	6	I/O	Input/output A4. Referenced to VCCA.
A5	7	I/O	Input/output A5. Referenced to VCCA.
A6	8	I/O	Input/output A6. Referenced to VCCA.
A7	9	I/O	Input/output A7. Referenced to VCCA.
A8	10	I/O	Input/output A8. Referenced to VCCA.
B1	21	I/O	Input/output B1. Referenced to VCCB.
B2	20	I/O	Input/output B2. Referenced to VCCB.
B3	19	I/O	Input/output B3. Referenced to VCCB.
B4	18	I/O	Input/output B4. Referenced to VCCB.
B5	17	I/O	Input/output B5. Referenced to VCCB.
B6	16	I/O	Input/output B6. Referenced to VCCB.
B7	15	I/O	Input/output B7. Referenced to VCCB.
B8	14	I/O	Input/output B8. Referenced to VCCB.
DIR	2	I	Direction-control signal.
GND	11,12,13	-	Ground
OEB	22	I	3-state output-mode enables. Pull OEB high to place all outputs in 3-state mode. Referenced to VCCA.
VCCA	1	-	A-port supply Voltage. $1.65V \leq VCCA \leq 5.5V$
VCCB	23,24	-	B-port supply Voltage. $1.65V \leq VCCB \leq 5.5V$

* All unused input pins cannot be suspended and suggested to connect to GND.

6 Voltage, Temperature, ESD and Thermal Ratings

6.1 Absolute Maximum Ratings ⁽¹⁾

Parameters			MIN	MAX	UNIT
Supply voltage range, VCCA, VCCB			-0.5	6.5	V
VI	Input voltage range ⁽²⁾	I/O ports (A port)	-0.5	6.5	V
		I/O ports (B port)	-0.5	6.5	
		Control inputs	-0.5	6.5	
VO	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾	A port	-0.5	6.5	V
		B port	-0.5	6.5	
VO	Voltage range applied to any output in the high or low state ⁽²⁾ ⁽³⁾	A port	-0.5	VCCA + 0.5	V
		B port	-0.5	VCCB + 0.5	
IIK	Input clamp current	VI < 0		-50	mA
IOK	Output clamp current	VO < 0		-50	mA
IO	Continuous output current			±50	mA
Continuous current through each VCCA, VCCB, and GND				±100	mA

(1)Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2)The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3)The output positive-voltage rating may be exceeded up to 6.5V maximum if the output current rating is observed.

6.2 ESD Ratings

ESD		Value	Unit
V(ESD)	Electrostatic discharge	Human-Body Model (HBM)	8 K
		Charged-device model(CDM)	2 K

(1)JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2)JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6 Voltage, Temperature, ESD and Thermal Ratings(Continued)

6.3 Recommended Operating Conditions⁽¹⁾ ⁽²⁾ ⁽³⁾ ⁽⁴⁾

			V _{CCI}	V _{CCO}	MIN	MAX	UNIT
V _{CCA}	Supply Voltage				1.65	5.5	V
V _{CCB}					1.65	5.5	
V _{IH}	High-level Input Voltage	Data Inputs	1.65V to 1.95V		V _{CCI} × 0.65		V
			2.3V to 2.7V		1.7		
			3V to 3.6V		2		
			4.5V to 5.5V		V _{CCI} × 0.7		
V _{IL}	Low-level Input Voltage	Data Inputs	1.65V to 1.95V			V _{CCI} × 0.35	V
			2.3V to 2.7V			0.7	
			3V to 3.6V			0.8	
			4.5V to 5.5V			V _{CCI} × 0.3	
V _{IH}	High-level Input Voltage	Control Inputs (referenced to V _{CCA})	1.65V to 1.95V		V _{CCA} × 0.65		V
			2.3V to 2.7V		1.7		
			3V to 3.6V		2		
			4.5V to 5.5V		V _{CCA} × 0.7		
V _{IL}	Low-level Input Voltage	Control Inputs (referenced to V _{CCA})	1.65V to 1.95V			V _{CCI} × 0.35	V
			2.3V to 2.7V			0.7	
			3V to 3.6V			0.8	
			4.5V to 5.5V			V _{CCI} × 0.3	
V _I	Input Voltage	Control Inputs			0	5.5	V
V _{I/O}	Input/Output voltage	Active State			0	V _{CCO}	V
		3-State			0	5.5	V
I _{OH}	High-Level Output Current		1.65V to 1.95V			-4	mA
			2.3V to 2.7V			-8	
			3V to 3.6V			-24	
			4.5V to 5.5V			-32	
I _{OL}	Low-Level Output Current		1.65V to 1.95V			4	mA
			2.3V to 2.7V			8	
			3V to 3.6V			24	
			4.5V to 5.5V			32	
Δt/Δv	Input Transition rise or fall	Data Inputs	1.65V to 1.95V			20	ns/V
			2.3V to 2.7V			20	
			3V to 3.6V			10	
			4.5V to 5.5V			5	
T _A	Operating Free-Air Temperature				-40	+125	°C

(1)V_{CCI} is the V_{CC} associated with the data input port.

(2)V_{CCO} is the V_{CC} associated with the output port .

(3)For V_{CCI} Values not specified in the data sheet, V_{IH} min=V_{CCI} × 0.7V, V_{IL} max=V_{CCI} × 0.3V .

(4)For V_{CCA} Values not specified in the data sheet, V_{IH} min=V_{CCA} × 0.7V, V_{IL} max=V_{CCA} × 0.3V.

7 Electrical Specifications

7.1 Electrical Characteristics^{(1) (2)}

over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS	V _{CCA}	V _{CCB}	MIN	TYP	MAX	UNIT
V _{OH}		I _{OH} =-100 μA, (V _I =V _{IH})	1.65V to 4.5V	1.65V to 4.5V	V _{CCO} -0.1			V
		I _{OH} =-4 mA, (V _I =V _{IH})	1.65V	1.65V	1.2			
		I _{OH} =-8 mA, (V _I =V _{IH})	2.3V	2.3V	1.9			
		I _{OH} =-24 mA, (V _I =V _{IH})	3V	3V	2.4			
		I _{OH} =-32 mA, (V _I =V _{IH})	4.5V	4.5V	3.8			
V _{OL}		I _{OL} =100 μA, (V _I =V _{IH})	1.65V to 4.5V	1.65V to 4.5V			0.1	V
		I _{OL} =4 mA, (V _I =V _{IH})	1.65V	1.65V			0.45	
		I _{OL} =8 mA, (V _I =V _{IH})	2.3V	2.3V			0.3	
		I _{OL} =24 mA, (V _I =V _{IH})	3V	3V			0.55	
		I _{OL} =32 mA, (V _I =V _{IH})	4.5V	4.5V			0.55	
I _I	DIR	V _I =V _{CCA} or GND	1.65V to 5.5V	1.65V to 5.5V			±2	μA
I _{off}	A or B Port	V _I or V _O =0 to 5.5V	0V	0 to 5.5V			±2	μA
			0 to 5.5V	0V			±2	
I _{OZ}	A or B Port	V _O =V _{CCO} or GND, OEB=V _{IH}	1.65V to 5.5V	1.65V to 5.5V			±2	μA
I _{CCA}		V _I =V _{CCI} or GND, I _O =0	1.65V to 5.5V	1.65V to 5.5V			15	μA
			5V	0V			15	
			0V	5V			-2	
I _{CCB}		V _I =V _{CCI} or GND, I _O =0	1.65V to 5.5V	1.65V to 5.5V			15	μA
			5V	0V			-2	
			0V	5V			15	
I _{CCA} + I _{CCB}		V _I =V _{CCI} or GND, I _O =0	1.65V to 5.5V	1.65V to 5.5V			25	μA
ΔI _{CCA}	A Port	One A Port at V _{CCA} -0.6V, DIR at V _{CCA} , B Port=Open	3V to 5.5V	3V to 5.5V			50	μA
	DIR	DIR at V _{CCA} -0.6V, B Port=Open, A Port at V _{CCA} or GND					50	
ΔI _{CCB}	B Port	One B Port at V _{CCB} -0.6V, DIR at GND, A Port=Open	3V to 5.5V	3V to 5.5V			50	μA
C _i	Control Inputs	V _I =V _{CCA} or GND	3.3V	3.3V		4	5	pF
C _{io}	A or B Port	V _O =V _{CCA/B} or GND	3.3V	3.3V		8.5	10	pF

(1)V_{CCO} is the V_{CC} associated with the output port.

(2)V_{CCI} is the V_{CC} associated with the input port.

7 Electrical Specifications (continued)

7.2 Switching Characteristics, $V_{CCA}=1.8V\pm 0.15V$

over recommended operating free-air temperature range, $V_{CCA}=1.8V\pm 0.15V$ (unless otherwise noted)

Parameter	From (Input)	To (Output)	$V_{CCB}=1.8V\pm 0.15V$		$V_{CCB}=2.5V\pm 0.2V$		$V_{CCB}=3.3V\pm 0.3V$		$V_{CCB}=5V\pm 0.5V$		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
TPLH	A	B	7.4	10.3	5.3	5.8	5.0	5.4	4.9	5.1	ns
TPHL											
TPLH	B	A	7.4	10.4	5.6	7.7	5.2	7.2	4.8	6.9	ns
TPHL											
TPHZ	OEB	A	11.0	11.8	8.3	12.0	7.6	12.3	6.6	12.7	ns
TPLZ											
TPHZ	OEB	B	10.5	12.3	9.9	10.5	9.9	10.6	10.6	11.3	ns
TPLZ											
TPZH	OEB	A	11.5	14.6	11.4	14.0	11.3	14.0	11.8	15.8	ns
TPZL											
TPZH	OEB	B	14.4	17.9	9.8	11.5	9.0	10.9	9.3	11.3	ns
TPZL											

7.3 Switching Characteristics, $V_{CCA}=2.5V\pm 0.2V$

over recommended operating free-air temperature range, $V_{CCA}=2.5V\pm 0.2V$ (unless otherwise noted)

Parameter	From (Input)	To (Output)	$V_{CCB}=1.8V\pm 0.15V$		$V_{CCB}=2.5V\pm 0.2V$		$V_{CCB}=3.3V\pm 0.3V$		$V_{CCB}=5V\pm 0.5V$		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
TPLH	A	B	5.7	7.5	2.9	3.9	2.5	3.3	2.3	2.9	ns
TPHL											
TPLH	B	A	5.3	6.1	2.9	3.9	2.4	3.5	1.9	3.2	ns
TPHL											
TPHZ	OEB	A	5.5	6.9	5.4	6.8	3.2	6.7	2.8	6.6	ns
TPLZ											
TPHZ	OEB	B	7.6	9.1	5.0	7.5	4.1	7.2	5.2	7.2	ns
TPLZ											
TPZH	OEB	A	4.6	10.8	4.7	6.4	4.7	6.5	5.0	7.0	ns
TPZL											
TPZH	OEB	B	11.6	13.4	5.6	7.3	4.6	6.1	4.0	5.2	ns
TPZL											

7 Electrical Specifications (continued)

7.4 Switching Characteristics, $V_{CCA}=3.3V\pm 0.3V$

over recommended operating free-air temperature range, $V_{CCA}=3.3V\pm 0.3V$ (unless otherwise noted)

Parameter	From (Input)	To (Output)	$V_{CCB}=1.8V\pm 0.15V$		$V_{CCB}=2.5V\pm 0.2V$		$V_{CCB}=3.3V\pm 0.3V$		$V_{CCB}=5V\pm 0.5V$		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
TPLH	A	B	5.2	7.0	2.4	3.5	2.0	2.9	1.8	2.5	ns
TPHL											
TPLH	B	A	5.3	6.1	2.9	3.9	2.4	3.5	1.9	3.2	ns
TPHL											
TPHZ	OEB	A	4.3	5.8	4.4	5.9	4.3	5.5	2.1	4.8	ns
TPLZ											
TPHZ	OEB	B	6.0	8.8	4.7	6.8	3.2	6.3	3.3	5.9	ns
TPLZ											
TPZH	OEB	A	3.3	10.0	3.4	6.0	3.4	4.8	3.4	5.4	ns
TPZL											
TPZH	OEB	B	11.2	13.0	5.0	6.8	3.9	5.5	3.3	4.6	ns
TPZL											

7.5 Switching Characteristics, $V_{CCA}=5V\pm 0.5V$

over recommended operating free-air temperature range, $V_{CCA}=5V\pm 0.5V$ (unless otherwise noted)

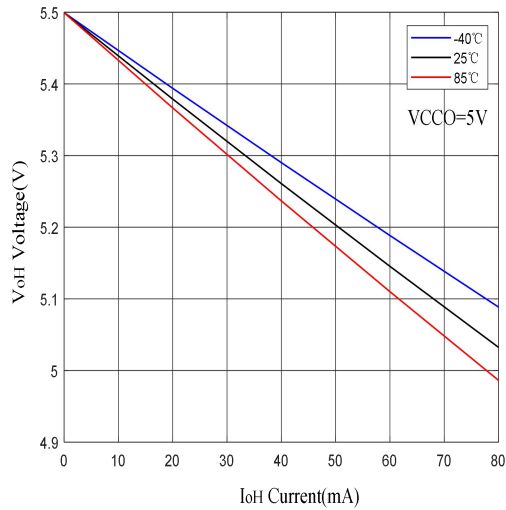
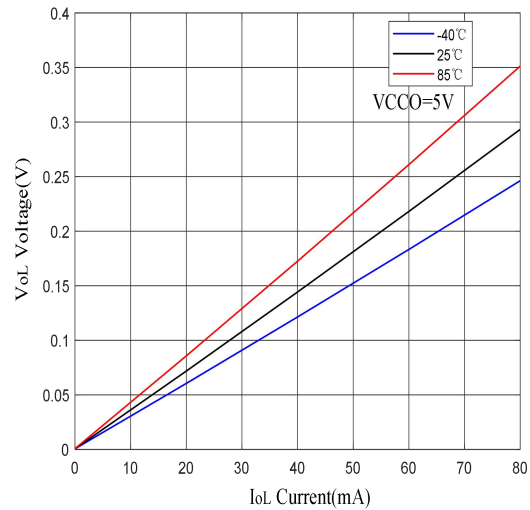
Parameter	From (Input)	To (Output)	$V_{CCB}=1.8V\pm 0.15V$		$V_{CCB}=2.5V\pm 0.2V$		$V_{CCB}=3.3V\pm 0.3V$		$V_{CCB}=5V\pm 0.5V$		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
TPLH	A	B	4.9	6.7	1.9	3.3	1.6	2.7	1.4	2.3	ns
TPHL											
TPLH	B	A	5.2	5.7	2.3	3.0	1.8	2.6	1.3	2.3	ns
TPHL											
TPHZ	OEB	A	3.3	4.3	3.1	3.8	3.3	4.5	2.6	3.7	ns
TPLZ											
TPHZ	OEB	B	3.3	9.5	3.8	4.9	3.3	4.3	1.7	3.8	ns
TPLZ											
TPZH	OEB	A	2.6	9.6	2.6	7.2	2.6	4.8	2.6	3.9	ns
TPZL											
TPZH	OEB	B	10.6	12.3	5.3	6.7	3.7	5.4	3.5	4.2	ns
TPZL											

7.6 Operating Characteristics

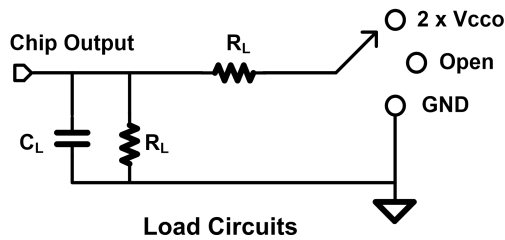
$T_A=25^\circ C$

Parameter		Test Conditions	$V_{CCA}=$ $V_{CCB}=1.8V$	$V_{CCA}=$ $V_{CCB}=2.5V$	$V_{CCA}=$ $V_{CCB}=3.3V$	$V_{CCA}=$ $V_{CCB}=5V$	UNIT
			TYP	TYP	TYP	TYP	
CpdA	A-port Input, B-port Output	CL=0, f=10 MHz, tr=tf=1 ns	2.4	2.6	2.9	3.7	pF
	B-port Input, A-port Output		16.7	17.3	17.9	22.6	
CpdB	A-port Input, B-port Output		16.4	17.1	17.8	22.5	
	B-port Input, A-port Output		2.4	2.6	2.9	3.9	

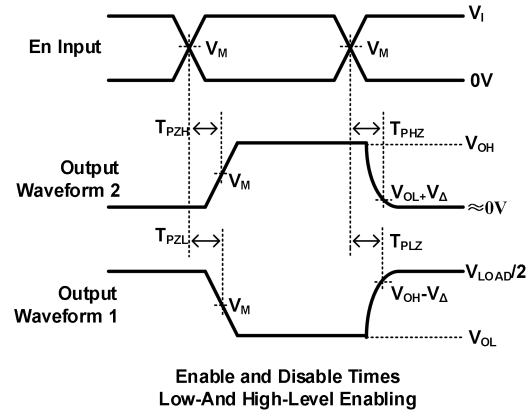
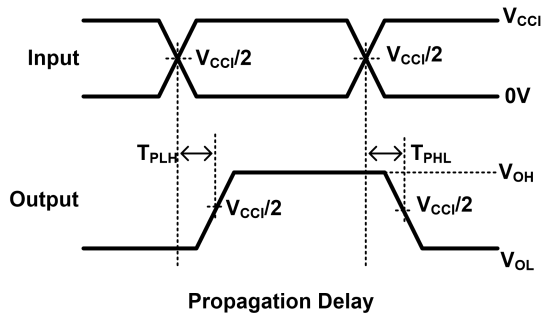
8 Typical Characteristics


Fig.8-1. Voltage Vs Current

Fig.8-2. Voltage Vs Current

9 Parameter Measurement Information



Parameter	VCCO	RL	CL	S1	VTP
T _{pd} Propagation Delay Time	1.8V-5.5V	2 kΩ	15 pF	Open	N/A
T _{PLZ} , T _{PZL} Enable Time, Disable Time	1.8V ± 0.15V	2 kΩ	15 pF	2 x VCCO	0.15V
	2.5V ± 0.2V	2 kΩ	15 pF	2 x VCCO	0.15V
	3.3V ± 0.3V	2 kΩ	15 pF	2 x VCCO	0.3V
	5V ± 0.5V	2 kΩ	15 pF	2 x VCCO	0.3V
T _{PHZ} , T _{PZH} Enable Time, Disable Time	1.8V ± 0.15V	2 kΩ	15 pF	GND	0.15V
	2.5V ± 0.2V	2 kΩ	15 pF	GND	0.15V
	3.3V ± 0.3V	2 kΩ	15 pF	GND	0.3V
	5V ± 0.5V	2 kΩ	15 pF	GND	0.3V


Notes:

- A.CL includes probe and jig capacitance.
- B.Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.
- C.All input pulses are supplied by generators having the following characteristics: PRR ≤ 10 MHz, Z_O = 50 Ω, dv/dt ≥ 1V/ns.
- D.The outputs are measured one at a time, with one transition per measurement.
- E.TPLZ and TPHZ are the same as tdis .
- F.TPZL and TPZH are the same as ten .
- G.TPLH and TPHL are the same as tpd .
- H.VCCI is the VCC associated with the input port.
- I.VCCO is the VCC associated with the output port.
- J.All parameters and waveforms are not applicable to all devices.

10 Detailed Description

The GT74LVC8T245 is an 8-bit, dual supply non-inverting Voltage level translation. Pin Ax and direction control pin are support by VCCA and pin Bx is support by VCCB. The A port is able to accept I/O Voltages ranging from 1.65V to 5.5V, while the B port can accept I/O Voltages from 1.65V to 5.5V. The high on DIR allows data transmission from A to B and a low on DIR allows data transmission from B to A. This device is fully specified for partial-power-down applications using I_{off}. The I_{off} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The VCC isolation feature ensures that if either VCC input is at GND, all outputs are in the high-impedance state. To ensure the high-impedance state during power up or power down, OEB should be tied to VCC through a pullup resistor; the minimum Value of the resistor is determined by the current-sinking capability of the driver.

The GT74LVC8T245 is a Voltage level translator that can operate from 1.65V to 5.5V (VCCA) and 1.65V to 5.5V (VCCB). The signal translation between 1.65V and 5.5V requires direction control and output enable control. When OEB is low and DIR is high, data transmission is from A to B. When OEB is low and DIR is low, data transmission is from B to A. When OEB is high, both output ports will be high-impedance.

Table.10-1. Function Table

Control Inputs		Output Circuits		Operation
OEB	DIR	A port	B port	
L	L	Enabled	Hi-Z	B TO A
L	H	Hi-Z	Enabled	A TO B
H	X	Hi-Z	Hi-Z	Isolation

11 Application Note

The GT74LVC8T245 device can be used in level-translation applications for interfacing devices or systems operating at different interface Voltages with one another. The maximum output current can be up to 32 mA when device is powered by 5V.

11.1 Typical Application

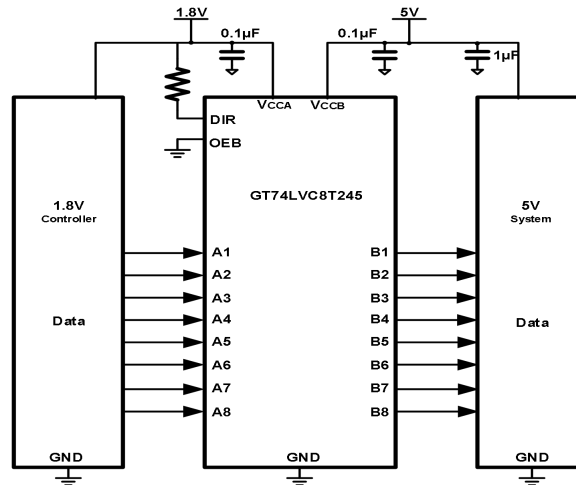


Fig.11-1. Typical Application Circuit

11.2 Enable Times

$$TPZH (DIR \text{ to } A) = TPLZ (DIR \text{ to } B) + TPLH (B \text{ to } A)$$

$$TPZL (DIR \text{ to } A) = TPHZ (DIR \text{ to } B) + TPHL (B \text{ to } A)$$

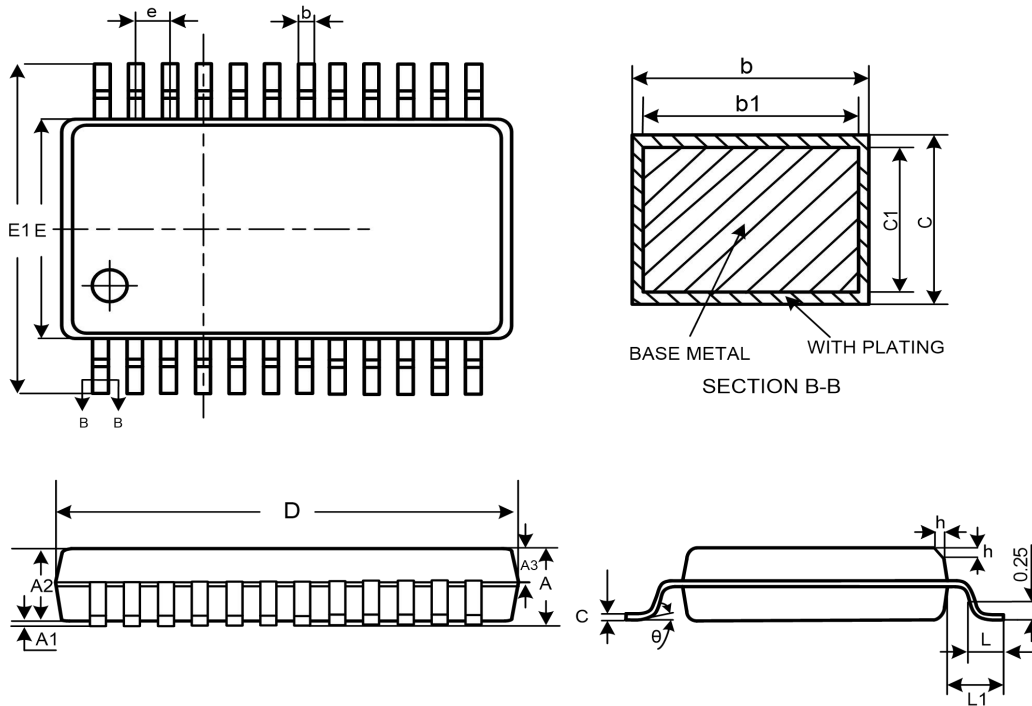
$$TPZH (DIR \text{ to } B) = TPLZ (DIR \text{ to } A) + TPLH (A \text{ to } B)$$

$$TPZL (DIR \text{ to } B) = TPHZ (DIR \text{ to } A) + TPHL (A \text{ to } B)$$

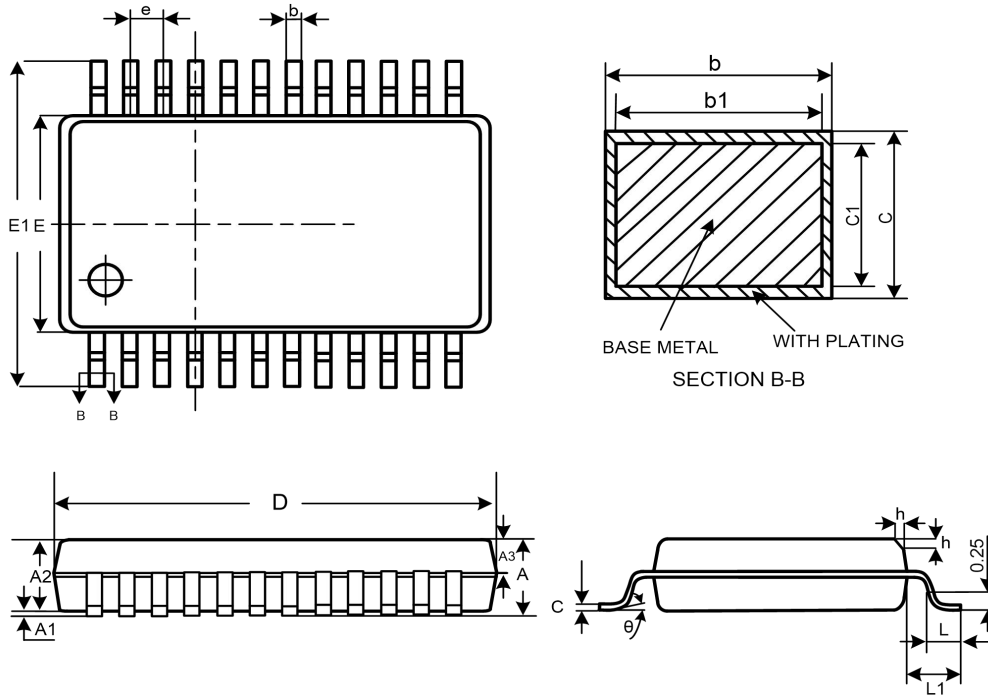
In a bidirectional application, these enable times provide the maximum delay from the time the DIR bit is switched until an output is expected. For example, if the GT74LVC8T245 initially is transmitting from A to B, then the DIR bit is switched; the B port of the device must be disabled before presenting it with an input. After the B port has been disabled, an input signal applied to it appears on the corresponding A port after the specified propagation delay.

12 Package Outline Dimension

SOP-24

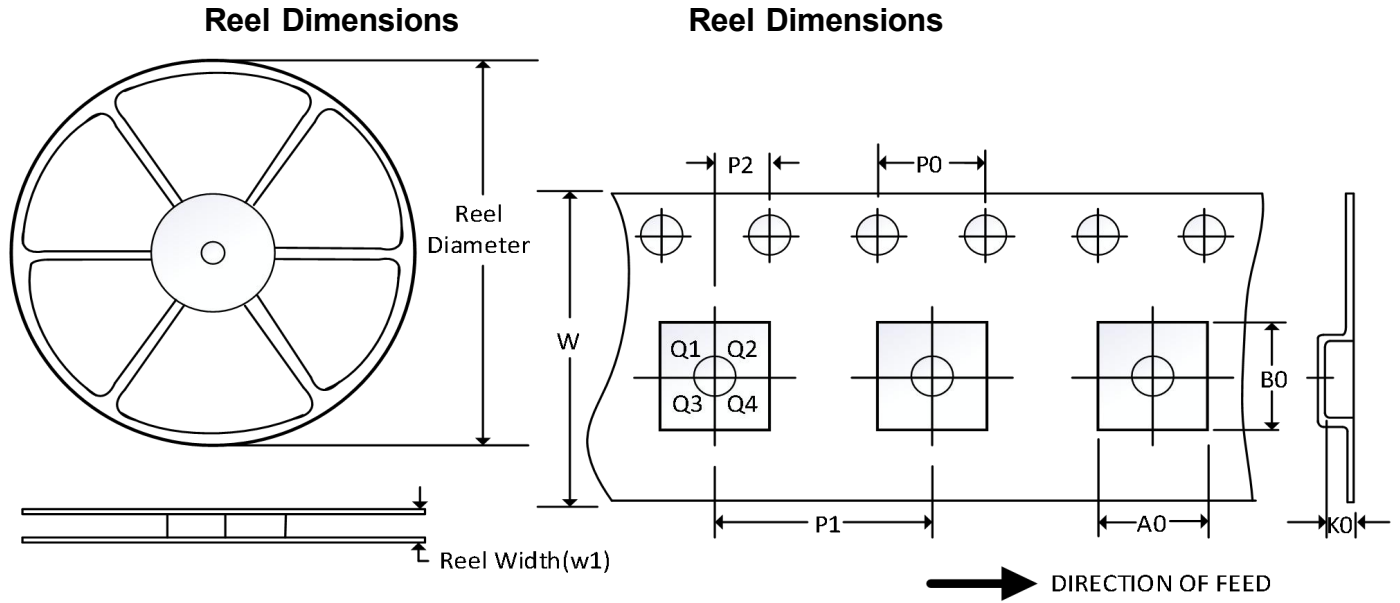


Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min	Nom	Max	Min	Nom	Max
A	2.36	2.54	2.64	0.093	0.100	0.104
A1	0.10	0.20	0.30	0.004	0.008	0.012
A2	2.26	2.30	2.35	0.089	0.091	0.093
A3	0.97	1.02	1.07	0.038	0.040	0.042
b	0.39	—	0.47	0.015	—	0.019
b1	0.38	0.41	0.44	0.015	0.016	0.017
c	0.25	—	0.29	0.010	—	0.011
c1	0.24	0.25	0.26	0.009	0.010	0.010
D	15.30	15.40	15.50	0.602	0.606	0.610
E1	7.40	7.50	7.60	0.291	0.295	0.299
E	10.10	10.30	10.50	0.398	0.406	0.413
e	1.27BSC			0.05BSC		
L	0.70	—	1.00	0.028	—	0.039
L1	1.40REF			0.055REF		
h	0.25	—	0.75	0.010	—	0.030
θ	0°	—	8°	0°	—	8°

12 Package Outline Dimension(continued)
TSSOP-24


Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min	Nom	Max	Min	Nom	Max
A	—	—	1.20	—	—	0.047
A1	0.05	—	0.15	0.002	—	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
A3	0.39	0.44	0.49	0.015	0.017	0.019
b	0.20	—	0.29	0.008	—	0.011
b1	0.19	0.22	0.25	0.007	0.009	0.010
c	0.13	—	0.18	0.005	—	0.007
c1	0.12	0.13	0.14	0.005	0.005	0.006
D	7.70	7.80	7.90	0.303	0.307	0.311
E1	4.30	4.40	4.50	0.169	0.173	0.177
E	6.20	6.40	6.60	0.244	0.252	0.260
e	0.65BSC			0.026BSC		
L	0.45	0.60	0.75	0.018	0.024	0.030
L1	1.00BCS			0.039BCS		
θ	0°	—	8°	0°	—	8°

13 Tape and Reel Information



Note: The picture is only for reference. Please make the object as the standard.

Key Parameter List of Tape and Reel

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TSSOP24	13"	16.4	6.95	8.30	1.60	4.0	8.0	2.0	16.0	Q1
SOIC-24(SOP24)	13"	24.4	10.85	16.15	2.85	4.0	16.0	2.0	24.0	Q1

Note:

- (1) All dimensions are nominal.
- (2) Plastic or metal protrusions of 0.15mm maximum per side are not included.