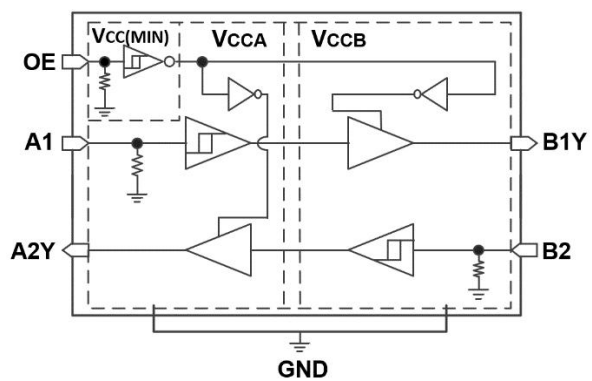


GTXU0202

2-Bit Fixed Direction Voltage-Level Translator

1 Features	2 Application
- Up to 200 Mbps support for 3.3 V to 5.0 V - Schmitt-trigger inputs allows for noisy inputs - High drive strength (up to 12 mA at 5 V) - Low power consumption - I_{off} supports partial-power-down mode operation - Operating temperature from -40°C to +125°C - Configurable dual-rail design allows each port to operate from 1.65 V to 5.5 V - Inputs with integrated static pull-down resistors - V_{CC} isolation and V_{CC} disconnect (I_{off}-float) feature - Control logic (OE) with V_{CC}(MIN) circuitry	- Eliminate slow or noisy input signals - Driving indicator LEDs or buzzers - General purpose I/O level shifting - Push-pull level shifting - Debouncing a mechanical switch

3 Description	Circuit Diagram
GTXU0202 is a 2-bit, dual-supply noninverting fixed direction voltage level translation device. Ax pins are referenced to V_{CCA} logic level, OE pin can be referenced to either V_{CCA} or V_{CCB} logic levels, and Bx pins are referenced to V_{CCB} logic levels. The A port is able to accept input voltages ranging from 1.65 V to 5.5 V, while the B port can also accept input voltages from 1.65 V to 5.5 V. Fixed direction data transmission can occur from A to B or B to A when OE is set to high in reference to either supply. When OE is set to low, all output pins are in the high-impedance state.	 The diagram shows the internal structure of the GTXU0202. It features two input ports, A and B, each with two pins (A1, A2 and B1, B2). The A pins are connected to a Schmitt-trigger input stage with a pull-down resistor to ground. The B pins are connected to a similar stage. The outputs of these stages are connected to a central logic block. The OE pin is connected to a control stage that can enable or disable the output drivers. The output drivers are connected to the A and B pins. The diagram also shows the power supply pins: V_{CCA}, V_{CCB}, and V_{CC}(MIN). The ground pin is labeled GND. Functional Block Diagram

4 Revision History

Revision	Date	Note
Rev. A1. 0	2024. 07. 26	Original Version

The latest datasheet version should be checked on the GTIC official website, as the company does not actively inform customers about updates to the datasheet.

5 Device Summary, Pin and Packages

Table 5-1. Device Summary⁽¹⁾

Serial Name	Part Name	Package	Body Size (Nom)	Marking ⁽²⁾	MSL ⁽³⁾	Package Qty
GTXU0202	GTXU0202V8	VSSOP8	3.20mmx2.10mmx1.90mm	0202 XXXXXXX	3	Tape and Reel,3000

(1) For all available packages, please contact product sales.

(2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

(4) "XXXXX" in Marking will be appeared as the batch code.

5 Device Summary, Pin and Packages(Continued)

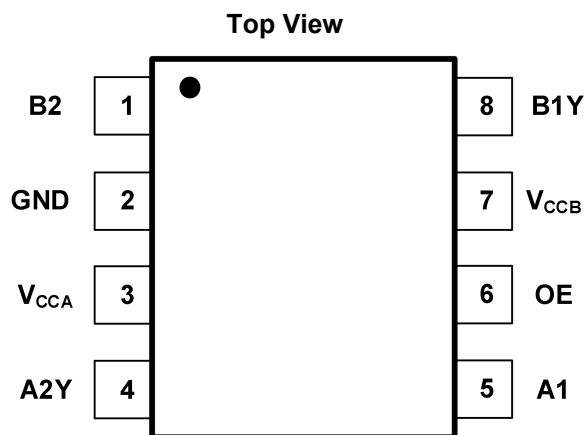


Fig.5-1. GTXU0202: V8 (VSSOP8) Package

Table 5-1 TD Pin Definition

PIN		I/O	Description
Name	V8		
B2	1	I	Input B2. Referenced to V_{CCB} .
GND	2	-	Ground
V_{CCA}	3	-	A-port supply voltage. $1.65\text{ V} \leq V_{CCA} \leq 5.5\text{ V}$
A2Y	4	O	Output A2. Referenced to V_{CCA} .
A1	5	I	Input A1. Referenced to V_{CCA} .
OE	6	I	Output Enable. Pull to GND to place all outputs in high-impedance mode. Pull to V_{CCA} or V_{CCB} to enable all outputs.
V_{CCB}	7	-	B-port supply voltage. $1.65\text{ V} \leq V_{CCB} \leq 5.5\text{ V}$
B1Y	8	O	Output B1. Referenced to V_{CCB} .

* It is suggested to leave the unconnected pins floating.

6 Voltage, Temperature, ESD and Thermal Ratings

6.1 Absolute Maximum Ratings⁽¹⁾

Parameters			Min.	Max.	Unit
V _{CC}	Supply voltage range		-0.5	6.5	V
V _I	Input voltage range ⁽²⁾		-0.5	6.5	V
V _O	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾		-0.5	6.5	V
V _O	Voltage range applied to any output in the high or low State ⁽²⁾⁽³⁾		-0.5	V _{CC} +0.5	V
I _{IK}	Input clamp current	V _I < 0		-50	mA
I _{OK}	Output clamp current	V _O < 0		-50	mA
I _O	Continuous output current			±50	mA
	Continuous current through V _{CC} or GND			±100	mA
T _J	Junction temperature		-55	150	°C
T _{stg}	Storage temperature		-55	150	°C

(1) Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3) The output positive-voltage rating may be exceeded up to 6.5 V maximum if the output current rating is observed.

6.2 ESD Ratings

ESD			Value	Unit
V(ESD)	Electrostatic discharge	Human-Body Model (HBM) ⁽¹⁾	8 K	V
		Charged-Device Model (CDM) ⁽²⁾	2 K	V

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

6 Voltage, Temperature, ESD and Thermal Ratings(Continued)

6.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted) ⁽¹⁾ ⁽²⁾ ⁽³⁾

			MIN	MAX	UNIT
V_{CCA}	Supply voltage A		1.65	5.5	V
V_{CCB}	Supply voltage B		1.65	5.5	V
I_{OH}	High-level output current	$V_{CCO}=1.65\text{ V}$		-4.5	mA
		$V_{CCO}=2.3\text{ V}$		-8	
		$V_{CCO}=3\text{ V}$		-10	
		$V_{CCO}=4.5\text{ V}$		-12	
I_{OL}	Low-level output current	$V_{CCO}=1.65\text{ V}$		4.5	
		$V_{CCO}=2.3\text{ V}$		8	
		$V_{CCO}=3\text{ V}$		10	
		$V_{CCO}=4.5\text{ V}$		12	
V_I	Input voltage ⁽³⁾		0	5.5	V
V_O	Output voltage	Active State	0	V_{CCO}	V
		Tri-State	0	5.5	V
T_A	Operating free-air temperature		-40	125	°C

(1) V_{CCI} is the V_{CC} associated with the input port.

(2) V_{CCO} is the V_{CC} associated with the output port.

(3)All control inputs and data I/Os of this device have weak pulldowns to ensure the line is not floating when undefined external to the device. The input leakage from these weak pulldowns is defined by the I_I specification indicated under Electrical Characteristics.

7 Electrical Specifications

7.1 Electrical Characteristics

over operating free-air temperature range (unless otherwise noted)

Parameter		Test Conditions	V _{CCA}	V _{CCB}	Operating Free-air Temperature (TA)			Unit	
					–40°C to 125°C				
					Min	Typ	Max		
VT+	Positive Going Input Threshold Voltage	Data Inputs (Ax, Bx) (Referenced to VCCI)	1.65V	1.65V	0.88		1.19	V	
			2.3V	2.3V	1.13		1.36		
			3V	3V	1.39		1.68		
			4.5V	4.5V	1.98		2.48		
			5.5V	5.5V	2.42		3.01		
		OE (Referenced to VCCA or VCCB)	1.65V	1.65V	1		1.29	V	
			2.3V	2.3V	1.26		1.69		
			3V	3V	1.52		2.03		
VT-	Negative Going Input Threshold Voltage	Data Inputs (Ax, Bx) (Referenced to VCCI)	4.5V	4.5V	2.16		2.78	V	
			5.5V	5.5V	2.63		3.33		
			1.65V	1.65V	0.41		0.57		V
			2.3V	2.3V	0.59		0.83		
			3V	3V	0.88		1.1		
		OE (Referenced to VCCA or VCCB)	4.5V	4.5V	1.4		1.73	V	
			5.5V	5.5V	1.74		2.15		
			1.65V	1.65V	0.44		0.63		
ΔVT	Input Threshold Hysteresis (VT+ – VT-)	Data Inputs (Ax, Bx) (Referenced to VCCI)	2.3V	2.3V	0.68		0.96	V	
			3V	3V	1.02		1.29		
			4.5V	4.5V	1.53		1.95		
			5.5V	5.5V	1.82		2.37		
			1.65V	1.65V	0.39		0.78		V
		2.3V	2.3V	0.43		0.75			
		3V	3V	0.45		0.66			
		4.5V	4.5V	0.53		0.79			
5.5V	5.5V	0.62		0.93					
OE (Referenced to VCCA or VCCB)	1.65V	1.65V	0.44		0.85	V			
	2.3V	2.3V	0.43		0.95				
	3V	3V	0.47		0.88				
	4.5V	4.5V	0.63		0.96				
	5.5V	5.5V	0.81		1.06				

7 Electrical Specifications(Continued)

over operating free-air temperature range (unless otherwise noted)

Parameter		Test Conditions	V _{CCA}	V _{CCB}	Operating Free-air Temperature (TA)			Unit
					-40°C to 125°C			
					Min	Typ	Max	
V _{OH}	High-Level Output Voltage	I _{OH} =-0.1mA	1.65V-5.5V	1.65V-5.5V	V _{CCO} -0.1			V
		I _{OH} =-4.5mA	1.65V	1.65V	1.41			
		I _{OH} =-8.0mA	2.3V	2.3V	2.15			
		I _{OH} =-10mA	3V	3V	2.85			
		I _{OH} =-12mA	4.5V	4.5V	4.35			
V _{OL}	Low-Level Output Voltage	I _{OL} =0.1mA	1.65V-5.5V	1.65V-5.5V			0.1	V
		I _{OL} =4.5mA	1.65V	1.65V			0.138	
		I _{OL} =8.0mA	2.3V	2.3V			0.182	
		I _{OL} =10mA	3V	3V			0.199	
		I _{OL} =8.0mA	4.5V	4.5V			0.143	
		I _{OL} =12mA	4.5V	4.5V			0.177	
I _I	Input Leakage Current	OE V _I =V _{CC} or GND	1.65V-5.5V	1.65V-5.5V	-0.1		2	μA
		Data Inputs(Ax,Bx) V _I =V _{CCi} or GND	1.65V-5.5V	1.65V-5.5V	-0.1		2	
I _{off}	Partial Power Down Current	A Port or B Port V _I or V _O =0 V - 5.5 V	0V	0V-5.5V	-2.5		2.5	μA
			0V-5.5V	0V	-2.5		2.5	
I _{off-float}	Floating Supply Partial Power Down Current	A Port or B Port V _I or V _O =GND	Floating	0V-5.5V	-2.5		2.5	μA
			0V-5.5V	Floating	-2.5		2.5	
I _{oz}	Tri-State Output Current	A or B Port: V _I =V _{CCi} or GND V _O =V _{CCO} or GND OE=GND	1.65V-5.5V	1.65V-5.5V	-2.5		2.5	μA
I _{CCA}	V _{CCA} Supply Current	V _I =V _{CCi} or GND I _O =0	1.65V-5.5V	1.65V-5.5V			6	μA
			0V	5.5V	-1			
			5.5V	0V			3	
		V _I =GND I _O =0	5.5V	Floating			15	
I _{CCB}	V _{CCB} Supply Current	V _I =V _{CCi} or GND I _O =0	1.65V-5.5V	1.65V-5.5V			6	μA
			0V	5.5V			3	
			5.5V	0V	-1			
		V _I =GND I _O =0	5.5V	Floating			15	
I _{CCA} + I _{CCB}	Combined Supply Current	V _I =V _{CCi} or GND I _O =0	1.65V-5.5V	1.65V-5.5V			6	μA
C _i	Control Input Capacitance	V _I =3.3 V or GND	3.3V	3.3V		10.2		PF

7 Electrical Specifications(Continued)

over operating free-air temperature range (unless otherwise noted)

Parameter		Test Conditions	V _{CCA}	V _{CCB}	Operating Free-air Temperature (TA)	Unit
					25°C	
					Typ	
C _{io}	Data I/O Capacitance	OE=GND, VO=1.65V DC +1 MHz -16 dBm Sine Wave	3.3V	3.3V	3.84	PF

7.2 Switching Characteristics: T_{sk}, T_{max}

over operating free-air temperature range (unless otherwise noted)

Parameter	Test Conditions		V _{CC1}	V _{CC0}	Operating Free-air Temperature (TA)			Unit
					-40°C to 125°C			
					Min	Typ	Max	
TMAX - Maximum Data Rate	50% Duty Cycle Input One Channel Switching 20% of Pulse> 0.7*V _{CC0} 20% of Pulse< 0.3*V _{CC0}	Up Translation	3.0V-3.6V	4.5V-5.5V	200			Mbps
			1.65V-1.95V	4.5V-5.5V	150			
			1.65V-1.95V	3.0V-3.6V	100			
		Down Translation	4.5V-5.5V	3.0V-3.6V	125			
			4.5V-5.5V	1.65V-1.95V	50			
			3.0V-3.6V	1.65V-1.95V	50			
Tsk - Output Skew	Timing Skew Between Any Switching Outputs on The Rising or Falling Edge	Up Translation	3.0V-3.6V	4.5V-5.5V			3	ns
			1.65V-1.95V	4.5V-5.5V			10	
			1.65V-1.95V	3.0V-3.6V			8	
		Down Translation	4.5V-5.5V	3.0V-3.6V			3	
			4.5V-5.5V	1.65V-1.95V			10	
			3.0V-3.6V	1.65V-1.95V			8	

7.3 Switching Characteristics, V_{CCA}=1.8V±0.15V

over operating free-air temperature range (unless otherwise noted)

Parameter		From	To	Test Conditions	B-Port Supply Voltage (V _{CCB})				Unit
					1.8V±0.15V		5V±0.5V		
					Min	Max	Min	Max	
t _{pd}	Propagation Delay Time	A	B	−40°C to 125°C	13.3	22.3	7.6	10.3	ns
t _{dis}	Disable Time	OE	B	−40°C to 125°C	16.7	36.3	11.7	22.2	ns
t _{en}	Enable Time	OE	B	−40°C to 125°C	15.8	41.7	11.5	23.5	ns

7.4 Switching Characteristics, V_{CCA}=5V±0.5V

over operating free-air temperature range (unless otherwise noted)

Parameter		From	To	Test Conditions	B-Port Supply Voltage (V _{CCB})				Unit
					1.8V±0.15V		5V±0.5V		
					Min	Max	Min	Max	
t _{pd}	Propagation Delay Time	A	B	−40°C to 125°C	7	13.7	2.1	2.8	ns
t _{dis}	Disable Time	OE	B	−40°C to 125°C	10.8	36.6	4.5	7.3	ns
t _{en}	Enable Time	OE	B	−40°C to 125°C	10	42	2.5	3.3	ns

7.5 Operating Characteristics

Parameter		Test Conditions(25°C)	Supply Voltage (V _{CCB} =V _{CCA})				Unit
			1.8±0.15V	2.5±0.2V	3.3±0.3V	5V±0.5V	
			Typ	Typ	Typ	Typ	
C _{pdA}	A to B: Outputs Enabled	A Port CL=0, RL=Open f=10 MHz t _{rise} =t _{fall} =5 ns	0.5	0.9	1.5	3.6	PF
C _{pdB}	A to B: Outputs Enabled	B Port CL=0, RL=Open f=10 MHz t _{rise} =t _{fall} =5 ns	3.9	5.7	8.5	15.8	PF

8 Typical Characteristics

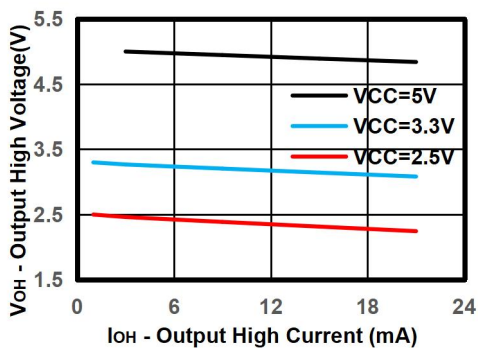


Fig.8-1. Typical ($T_A=25^\circ C$) Output High Voltage (V_{OH}) vs Source Current (I_{OH})

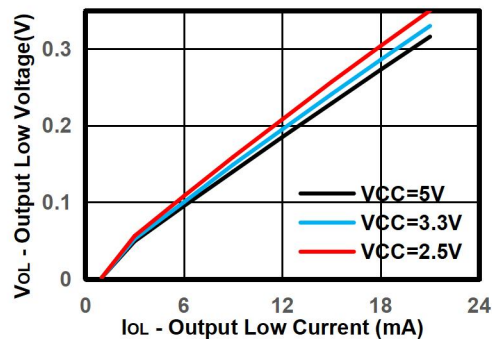


Fig.8-2. Typical ($T_A=25^\circ C$) Output Low Voltage (V_{OL}) vs Sink Current (I_{OL})

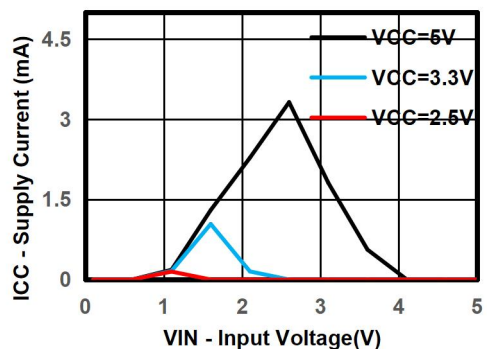
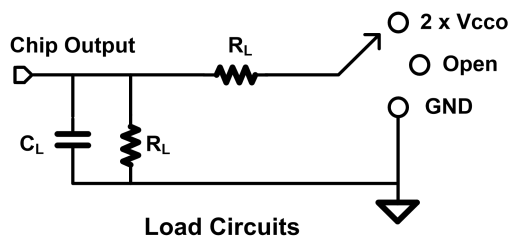
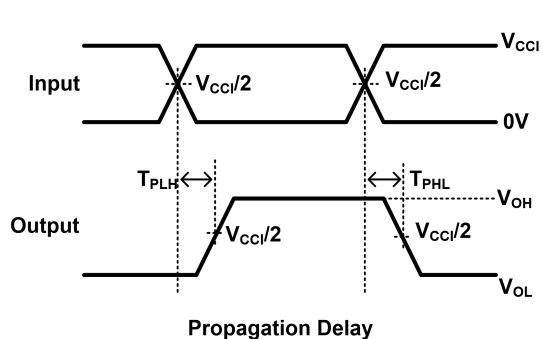


Fig.8-3. Typical ($T_A=25^\circ C$) Supply Current (I_{CC}) vs Input Voltage (V_{IN})

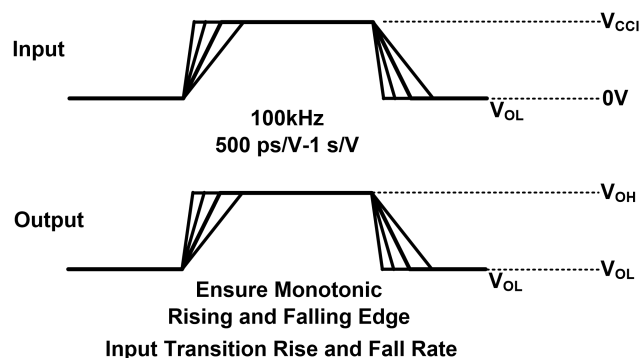
9 Parameter Measurement Information



Parameter	V_{CCO}	R_L	C_L	S1	VTP
T_{pd} Propagation delay time	1.65V-5.5V	10 k Ω	5 pF	Open	N/A
T_{en}, T_{dis} Enable time, disable time	1.65V-2.7V	10 k Ω	5 pF	$2 \times V_{CCO}$	0.15V
	3.0V-5.5V	10 k Ω	5 pF	$2 \times V_{CCO}$	0.3V
T_{en}, T_{dis} Enable time, disable time	1.65V-2.7V	10 k Ω	5 pF	GND	0.15V
	3.0V-5.5V	10 k Ω	5 pF	GND	0.3V

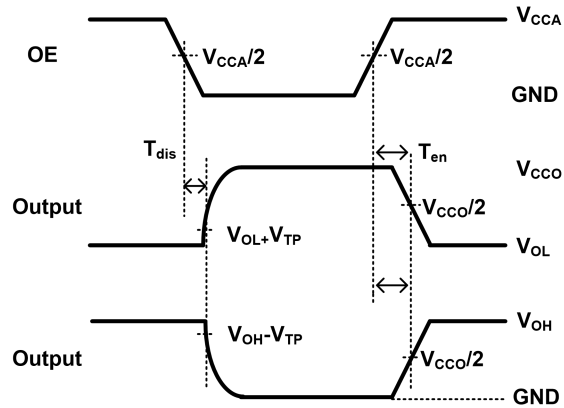


1. V_{CCI} is the supply pin associated with the input port.
2. V_{OH} and V_{OL} are typical output voltage levels that occur with specified R_L , C_L , and S1.



1. V_{CCI} is the supply pin associated with the input port.
2. V_{OH} and V_{OL} are typical output voltage levels that occur with specified R_L , C_L , and S1.

9 Parameter Measurement Information(Continued)



Enable and Disable Times
Low-And High-Level Enabling

1. Output waveform on the condition that input is driven to a valid Logic Low.
2. Output waveform on the condition that input is driven to a valid Logic High.
3. V_{CCO} is the supply pin associated with the output port.
4. V_{OH} and V_{OL} are typical output voltage levels with specified R_L , C_L , and $S1$.

10 Detailed Description

The GTXU0202 is a 2-bit translating transceiver that uses two individually configurable power-supply rails. The device is operational with V_{CCA} and V_{CCB} supplies as low as 1.65 V and as high as 5.5 V. Additionally, the device can be operated with $V_{CCA}=V_{CCB}$. The A port is designed to track V_{CCA} , and the B port is designed to track V_{CCB} .

The GTXU0202 device is designed for asynchronous communication between data buses, and transmits data with fixed direction from the A bus to the B bus on some channels and from the B bus to the A bus on the remaining channels. The output-enable input (OE) is used to disable the outputs so the buses are effectively isolated. The output-enable pin of the GTXU0202 (OE) can be referenced to either V_{CCA} or V_{CCB} . The OE pin can be left floating or externally pulled down to ground to ensure the high-impedance state of the level shifter outputs during power up or power down.

This device is fully specified for partial-power-down applications using the I_{off} current. The I_{off} protection circuitry ensures that no excessive current is drawn from or sourced into an input or output while the device is powered down.

Table. 10-1. Function table

Control Input OE	Port Status		Operation
	Input	Output	
H	L	L	Unidirectional non-inverting voltage translation
H	H	H	Unidirectional non-inverting voltage translation
L	X	Hi-z	Isolation

11 Application Note

The GTXU0202 device can be used in level-translation applications for interfacing devices or systems operating at different interface voltages with one another. The GTXU0202 device is ideal for use in applications where a push-pull driver is connected to the data Inputs. The maximum data rate can be up to 200 Mbps when the device translates a signal from 3.3 V to 5.0 V.

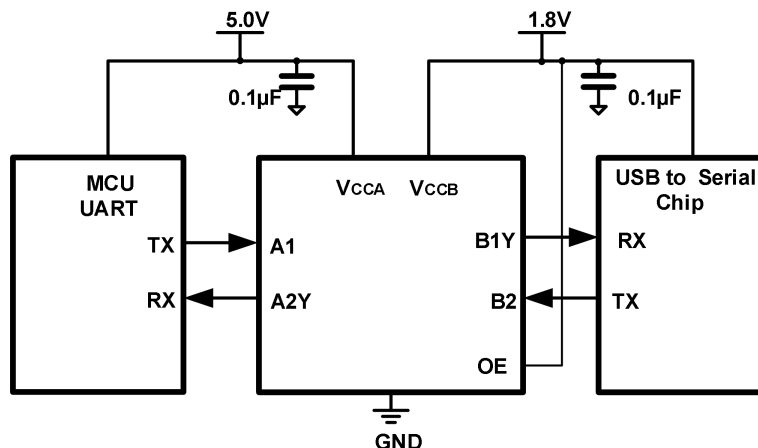
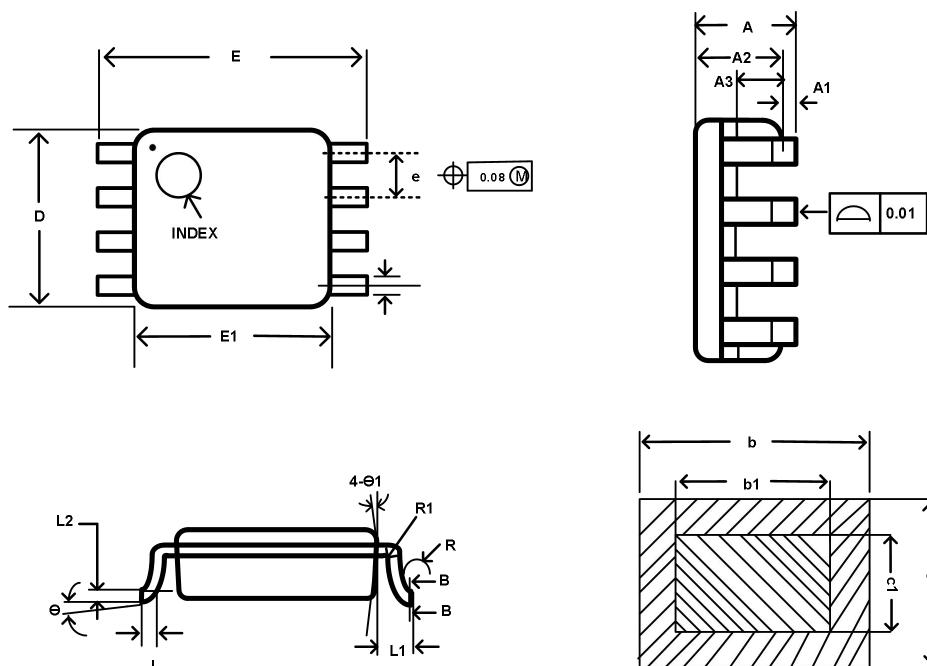


Fig.11-1. Typical Application Schematic

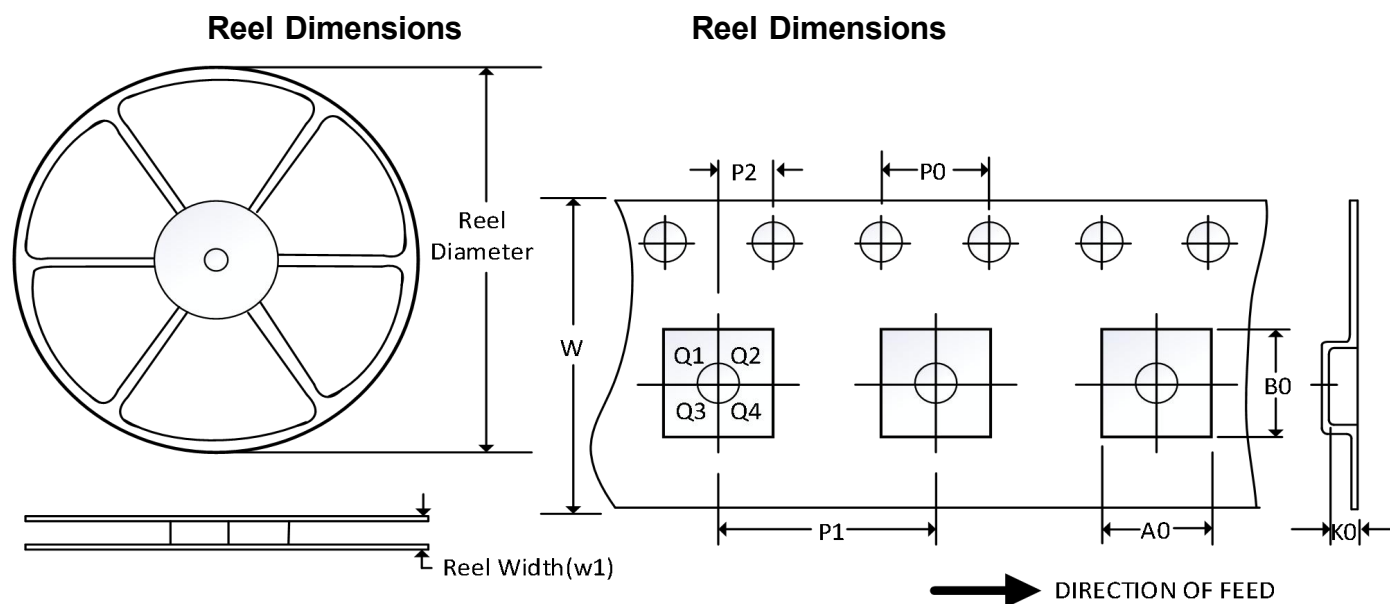
12 Package Outline Dimension

VSSOP8



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min	Nom	Max	Min	Nom	Max
A	-	-	0.90	-	-	0.035
A1	0	0.05	0.10	0.000	0.002	0.004
A2	0.65	0.75	0.80	0.026	0.030	0.031
A3	0.32	0.37	0.42	0.013	0.015	0.017
b	0.17	-	0.27	0.007	-	0.011
b1	0.17	0.20	0.23	0.007	0.008	0.009
c	0.10	-	0.18	0.004	-	0.007
c1	0.10	0.13	0.14	0.004	0.005	0.006
D	1.90	2.00	2.10	0.075	0.079	0.083
E	3.00	3.10	3.20	0.118	0.122	0.126
E1	2.20	2.30	2.40	0.087	0.091	0.094
e	0.40	0.50	0.60	0.016	0.020	0.024
L	0.20	0.26	0.35	0.008	0.010	0.014
L1	0.40REF			0.016REF		
L2	0.12BSC			0.005BSC		
R	0.07	-	-	0.003	-	-
R1	0.07	-	-	0.003	-	-
θ	0°	-	6°	0°	-	6°
θ1	9°	12°	15°	9°	12°	15°

13 Tape and Reel Information



Note: The picture is only for reference. Please make the object as the standard.

Key Parameter List of Tape and Reel

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 QuAdrant
VSSOP8	7"	9.5	2.25	3.35	1.4	4	4	2	8	Q3

Note:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.